Ethernity Patent List

Patent #	Patent Title	Description (Primary Claims)
7,924,863	**Device & Method for Processing Data Chunks**	Methods **and devices for managing data chunks, especially in multi-port aggregation devices, which dramatically decrease the logic needed for multiple port Ethernet/IP aggregation implementation in FPGA or ASIC. Also applies to the support of any other multiple port design including ATM , PPP, or TDM**
7,822,051 **8,223,788**	**Method & System for Transmitting Packets** **Method and system for queuing descriptors**	Mechanism that **enables deterministic, zero latency multicast technology of unlimited duplications, with the ability to maintain Quality of Services (QoS)** between unicast and multicast packets, together with the ability to assign a different packet encapsulation/header per duplication. Also applies to the **decrease of the internal chip memory by the use of external DDR memory** that holds pointer and descriptor information, **while maintaining wire speed duplication and zero latency**.
US 7,620,770	**Device & Method for Storing and Processing Data Units**	**Zero copy mechanism for storing an ingress data unit at the DDR memory** unit **for the purposes of packet re-ordering and traffic management**, then **retrieving a fetched data unit from the DDR memory for data unit processing**, wherein the fetched data unit comprises at least a portion of the ingress data unit, all **at wire speed and with single read and write to/from external DDR memory**.
8,345,685	**Method and device for processing data packets**	The invention relates to methods and devices performing **advanced search mechanism** to process data packets and perform various searches on a data packet especially for forwarding data packets by layer two switches, using **low cost DDR instead of expensive TCAM**
US10966112B2	**Method and System for distribution of high data throughput over multiple wireless connections**	A method that enables to distribute **high capacity data payload** over multiple lower speed links. The fragment can run over standard routers located in the network up to the far end destination **without a need for any gateway** devices between the two end point – **applicable for SDWAN that perform load balancing between low speed link over the internet , wireless distribution**
US12425341B2		***Low Latency Single-Clock LPM Engine*** **(recently granted**